



PIN CONNECTIONS

Pin	Symbol	Level	Function
1	/CS	L	Chip select
2	/REST	L	Reset signal ,active “L”
3	A0	H/L	H: Display data L: Control data
4	SCK	H/L	Serial clock input
5	SDA	H/L	Serial data input
6	V _{DD}	3.0V	Power Supply for logic
7	V _{SS}	0V	GND
8	VB0+	-	LCD Bias Voltages .These are voltage sources to provide SEG drving currents.These voltages are generated internally.Connect capacitors of CBX value between VBX+ and VBX-.
9	VB0-	-	
10	VB1+	-	
11	VB1-	-	
12	VLCD	-	High voltage LCD suppluy connect a capacitor to VSS.

NOTES: 1. Built-in UC1701X controller
2. FSTN、COG

MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	39.4X57.5X2.0	mm
Viewing Area (W x H)	35.4 X28.5	mm
Dot Pitch (W x H)	0.32X0.40	mm
Dot Size (W x H)	0.29X0.37	mm

ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	V _{DD} -V _{SS}	-0.3	4.0	V
Supply Voltage (LCD)	V ₀	-0.3	13.2	V
Input Voltage	V _I	-0.3	V _{DD} +0.3	V
Operating Temp.	T _{OPR}	-20	70	°C
Storage Temp.	T _{STG}	-30	80	°C

ELECTRICAL CHARACTERISTICS (V_{DD}=3.3V, T_a=25°C)

Item	Sym.	Min.	Typ.	Max.	Unit
Input High Voltage	V _{IH}	0.8V _{DD}	-	V _{DD}	V
Input Low Voltage	V _{IL}	0	-	0.2V _{DD}	V
Output High Voltage	V _{OH}	0.8V _{DD}	-	V _{DD}	V
Output Low Voltage	V _{OL}	0	-	0.2V _{DD}	V
Supply Current	I _{DD}	-	TBD	-	mA
LCD Driving Voltage	V ₀	-	9.0	-	V