



PIN CONNECTIONS

Pin	Symbol	Level	Function
1	NC	L	No connection
2	CS0	L	Chip select
3	RST	H/L	Reset signal ,active L
4	CD	H/L	H: Display data L: Control data
5	SCK	H/L	Serial clock input
6	SDA		Serial data input
7	V _{DD}	3.3V	Power Supply for logic
8	V _{SS}	0V	GND
9	VB0+	-	LCD Bias Voltages . These are voltage sources to provide SEG driving currents. These voltages are generated internally. Connect capacitors of CBX value between VBX+ and VBX-.
10	VB0-	-	
11	VB1+	-	
12	VB1-	-	
13	V _{LCDIN}	-	Main LCD power supply.High voltage
14	V _{LCDOUT}	-	LCD supply connect a capacitor to VSS.

MECHANICAL DATA

Item	Nominal Dimensions	Unit
Module Size (W x H x T)	42.0X44.0X2.0	mm
Viewing Area (W x H)	39.0 X16.2	mm
Dot Pitch (W x H)	0.29X0.43	mm
Dot Size (W x H)	0.26X0.40	mm

ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	V _{DD} -V _{SS}	-0.3	4.0	V
Supply Voltage (LCD)	V ₀	-0.3	13.2	V
Input Voltage	V _I	-0.3	V _{DD} +0.3	V
Operating Temp.	T _{OPR}	0	50	°C
Storage Temp.	T _{STG}	-10	60	°C

ELECTRICAL CHARACTERISTICS (V_{DD}=5.0V, Ta=25°C)

Item	Symbol	Min.	Typ.	Max.	Unit
Input High Voltage	V _{IH}	0.8V _{DD}	-	V _{DD}	V
Input Low Voltage	V _{IL}	0	-	0.2V _{DD}	V
Output High Voltage	V _{OH}	0.8V _{DD}	-	V _{DD}	V
Output Low Voltage	V _{OL}	0	-	0.2V _{DD}	V
Supply Current	I _{DD}	-	TBD	-	mA
LCD Driving Voltage	V ₀	-	8.0	-	V

NOTES: 1. Built-in Uc1701x controller